

REGISTRATION FORM

**FACULTY DEVELOPMENT PROGRAMME
ON
HIGH-PERFORMANCE COMPUTING
FOR
MACHINE LEARNING & DEEP LEARNING**

- 1 Name
- 2 Designation
- 3 Department
- 3 Name of Institution
- 4 Mobile No
- 5 e-mail
- 6 Food Preference
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 Non-Veg

Declaration

I hereby declare that the information furnished above is true to the best of my knowledge. I agree to abide by the rules and regulations governing the programme. If selected, I shall attend the course for the entire duration. I also undertake the responsibility to inform the co-ordinators sufficiently in advance, in case I am unable to attend the course.

Place:

Date :

Signature of Applicant

Last Date of Application	15/02/2026
Intimation of Selection	18/02/2026
Confirmation of Participation	20/02/2026

Address for Correspondence:

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Resource Person:

Eminent faculty from national level institutes and industry handles the sessions.

Eligibility

Faculty members of Govt. Engineering Colleges of the state who are interested in machine learning and deep learning research.

How to apply

The application included herewith or in a similar format, duly recommended by the head of the Institution should be uploaded through the following link <https://forms.gle/uVGuyvGEwCSUA2bUA>. The selected candidates will be intimated through email. The number of seats is limited to 30.

There will be no registration fee. TA will be provided to participants as per Government rules.

**FACULTY DEVELOPMENT PROGRAMME
ON
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SPONSORED BY



**DIRECTORATE OF TECHNICAL
EDUCATION
GOVERNMENT OF KERALA**

(24/02/2026 TO 26/02/2026)

ORGANIZED BY



**DEPARTMENT OF
COMPUTER SCIENCE & ENGINEERING
COLLEGE OF ENGINEERING
TRIVANDRUM**

Introduction

Director of Technical Education, Government of Kerala, has entrusted College of Engineering Trivandrum (CET) to organize courses and training programmes for the faculty and staff of engineering colleges and polytechnic institutions in new and emerging areas of engineering and research.

For promoting research activities under the areas of Machine Learning and Deep Learning, a High Performance Computing (HPC) system (NVIDIA DGX A100) was installed at College of Engineering Trivandrum in the year 2024. The system is currently being used by B.Tech, M. Tech Students, Research Scholars and Faculties from all Govt. Engineering Colleges of the state. Currently around 150 user accounts are created in HPC System. To provide HPC access to users from outside CET campus, VPN security is implemented through Checkpoint Firewall.

College of Engineering Trivandrum

College of Engineering Trivandrum was established in 1939 as the first engineering college in the Travancore State. Over the past 85+ years it has grown to the most sought -after institute of engineering studies in the State of Kerala. The college has eight full-fledged engineering departments offering 9 undergraduate, 29 postgraduate and Ph. D. programmes under the University of Kerala and Kerala Technological University. The college has more than 3500 students, 280+ teaching staff and 250+ non - teaching staff. Ever since the inception, the college has maintained its commitment to high quality education. It has a vast and strong alumni base, spread across the globe.

Department of Computer Science & Engineering

The department was started in the year 1984. From its humble beginnings, the department has grown tremendously and has evolved into one of the foremost units of the College. The department presently offers regular B.Tech program (two batches), part time B.Tech program three M. Tech. programs and research programme. The department has a long tradition of producing technically competent graduates and the alumni of the department are often high achievers, occupying prominent positions in the industry as well as the academia in India and abroad.

Course Objective:

This Faculty Development Programme (FDP) is designed to equip participants with the knowledge and hands-on skills required to leverage High-Performance Computing (HPC) for cutting-edge Machine Learning (ML) and Deep Learning (DL) projects. With the exponential growth of data and the increasing complexity of AI models, HPC systems play a critical role in enabling faster training, large-scale experimentation, and efficient deployment.

The programme introduces participants to HPC architectures, parallelization strategies, distributed training frameworks, and resource management techniques. Participants will gain practical exposure to setting up ML/DL environments on HPC clusters, training deep neural networks across multiple GPUs/CPU, and handling large-scale datasets. Real-world case studies from domains such as natural language processing, computer vision, climate modeling, and genomics will be explored to highlight the impact of HPC in AI-driven research.

By the end of the programme, participants will be able to:

- * Understand HPC fundamentals and their applications in ML/DL
- * Utilize parallel and distributed training methods effectively
- * Optimize computation and resource usage in HPC environments
- * Implement large-scale ML/DL pipelines on HPC systems
- * Apply HPC-powered AI techniques to real-world datasets and research problems

Syllabus:

1. Introduction to High-Performance Computing (HPC) for AI
2. Parallelization Techniques in Machine Learning
3. Deep Learning on HPC Systems
4. Big Data and HPC for ML/DL Projects
5. Case Studies and Hands-On Projects

SPONSORSHIP CERTIFICATE

Certified that Dr./ Prof.....
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Is a Professor/ Associate Professor/ Assistant Professor of this institution and is hereby sponsored for the FDP on High-Performance Computing for Machine Learning and Deep learning' at College of Engineering Trivandrum from 24/02/2026 to 26/02/2026. If selected, he/she will be permitted to attend the course.

Place:

Date:

Name & Signature
of the Sponsoring Authority

(Seal of the Institution)