

AICTE Sponsored

Two Weeks Faculty Development Programme on

Modern Trends in Power Electronics and Drives

1st to 14th July 2019

Co-ordinators:

Dr. Mayadevi N., Dr. Mini V. P. & Dr. Hari Kumar R.



Organised by



Dept. of Electrical Engineering College of Engineering Trivandrum Thiruvananthapuram Kerala - 695 016 www.cet.ac.in

About College of Engineering Trivandrum

College of Engineering Trivandrum (CET) was established in 1939 as the first engineering college in the then Travancore state. Over the past 79 years it has grown to the most sought-after institute for engineering studies in the state of Kerala. The College has eight full-fledged engineering departments offering undergraduate, post-graduate and Ph.D. programmes under the A.P.J. Abdul Kalam Technological University (KTU).



Ever since the inception, the college has maintained its commitment to high quality education. It has a vast and strong alumini base, spread across the world.

About the Department

Department of Electrical Engineering, College of Engineering Trivandrum began its operations along with the establishment of the college in 1939. The department offers an undergraduate B.Tech. programme in Electrical & Electronics Engineering and four M. Tech. programmes (Control Systems, Power Systems, Electrical Machines and Guidance & Navigational Control). The department is recognised as a QIP centre for Ph.D. programme by the MHRD, Government of India.

Objectives of the Programme

This course is envisaged to inculcate the design and hardware implementation of power electronic systems to the teaching fraternity. The participants will get hands on exposure to the design, simulation and hardware implementation of power electronic circuits, drives and controllers. This course will provide an insight into:

- ♦Modern trends in power electronics and drives
- ◆Recent advances in DC-DC converters
- ♦Multilevel converters and its PWM techniques
- ♦ Design of controllers for power electronic systems
- ♦ Application of optimization techniques in controller design
- ♦Real-time simulation of power electronic converters and drives
- ♦Embedded controller design for power electronic converters
- ♦ Design and implementation of grid connected converters
- ♦Power electronic systems for power quality enhancement
- ♦ Hardware-in-Loop simulation of motor drives
- ♦ Hardware implementation of power electronic systems

Resource Persons

Technical sessions will be handled by eminent resource persons from leading institutions like IIT, IISc, IIST, NIT, CET, CDAC and industry.

Eligibility

The FDP is open for faculty members of engineering colleges recognised by All India Council for Technical Education (AICTE) who send their application through proper channel. There is no course fee for them. All registered participants will be provided with kit and course material. Participation certificates will be issued to all the participants who have successfully completed the programme.

Registration

Fill the online form at https://bit.ly/AICTEFDPMTPED. The scanned copy of the signed sponsorship certificate has to be uploaded in the online form. Maximum number of seats is limited to 40.

TA/ Accommodation

Selected sponsored faculty will be paid TA subject to the limit of Three tier AC train/ bus fare by the shortest route from the place of work to Thiruvananthapuram and back. However, the maximum TA payable is Rs.3000/-. They will be provided with boarding and loadging at CET hostels on prior request.

Self Supporting

Non-sponsored AICTE approved college teachers and other self-support faculty, scientists from research labs, practicing engineers from industries and other interested are also eligible for applying. They should pay a fees as below.

Faculty: Rs. 5000+18% GST

Industry participants: Rs. 8000+18% GST

Important Dates

Last date for receipt of application: 20-06-2019

Intimation of selection: 21-06-2019

Address for Correspondance

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Registration Form

1. Name:
2. Age:4. Qualification:
5. Designation:
6. Name of Institution:
7. Department:
8. Address for Communication:
9. Mobile No.:
10. Email Address:
11. Experience in Years: Teaching: Others:
The information provided herewith is true to the best of my knowledge. I agree to abide by the rules and regulations governing the faculty development programme.
Place:
Date: Signature of the Applicant
Sponsorship Certificate
Certified that Mr./ Ms./ Dr

Place: Name & Signature of the Date: sponsoring Authority (Seal of the Institution)