

Faculty and Staff Development Training Center **College of Engineering Trivandrum**

DTE Sponsored

One Week Faculty Development Programme on

Embedded Systems Design for Power Electronic Converters

3rd to 7th December 2018

Co-ordinators:

Dr. Mini V. P. & Dr. Mayadevi N.



Organised by



Dept. of Electrical Engineering
College of Engineering Trivandrum
Thiruvananthapuram Kerala - 695 016

www.cet.ac.in

About College of Engineering Trivandrum

College of Engineering Trivandrum (CET) was established in 1939 as the first engineering college in the then Travancore state. Over the past 79 years it has grown to the most sought-after institute for engineering studies in the state of Kerala. The College has eight full-fledged engineering departments offering undergraduate, post-graduate and Ph.D. programmes under the A.P.J. Abdul Kalam Technological University (KTU).



Ever since the inception, the college has maintained its commitment to high quality education. It has a vast and strong alumni base, spread across the world.

About the Department

Department of Electrical Engineering, College of Engineering Trivandrum began its operations along with the establishment of the college in 1939. The department offers an undergraduate B.Tech. programme in Electrical & Electronics Engineering and four M. Tech. programmes (Control Systems, Power Systems, Electrical Machines and Guidance & Navigational Control). The department is recognised as a QIP centre for Ph.D. programme by the MHRD, Government of India.

Objectives of the Programme

This course is envisaged to inculcate the design and hardware implementation of controllers for power electronic converters to the teaching fraternity. The participants will get hands on exposure to the design, simulation and hardware implementation of FPGA based controllers for power electronic circuits and drives. This course will provide an insight into:

- ◆ Design of controllers for power electronic systems
- ◆ Modelling of systems using C language
- ◆ Implementation of controllers in FPGA platform
- ◆ Real-time simulation
- ◆ Hardware-in-Loop simulation
- ◆ Full spectrum simulation
- ◆ Introduction to LabVIEW
- ◆ Controller implementation using National Instruments tools

Resource Persons

The resource persons consist of faculty members from National Institutes, CDAC and industry.

Eligibility

The FDP is open for faculty members of Government, Aided and Self financing engineering colleges in Kerala. Faculty members from EEE, EIE and ECE can attend the program. All registered participants will be provided with kit and course material. Participation certificates will be issued to all the participants who have attended the FDP on all days.

Registration

Fill the online form at <https://bit.ly/2ODqdl5>. The scanned copy of the signed sponsorship certificate has to be uploaded in the online form. Maximum number of seats is limited to 30 and the selection is based on first come first served basis.

TA/ Accommodation

TA will be provided as per Government norms to faculty from Government/ Government aided institutions. Accommodation can be arranged on prior request. Working lunch and refreshments will be provided for all participants during the FDP.

Important Dates

Last date for receipt of application: **28-11-2018**

Intimation of selection: **29-11-2018**

Address for Correspondance

Dr. Mini V. P.

Associate Professor in Engineering Engineering,
College of Engineering Trivandrum, Thiruvananthapuram - 695020
e-mail: fdpeecet@gmail.com, Mobile: 9895904715

Sponsorship Certificate

Certified that Mr./ Ms./ Dr. is an employee of this institution and is hereby sponsored for the FDP on **Embedded Systems Design for Power Electronic converters** at College of Engineering Trivandrum during 3rd - 7th December 2018. He/ she will be permitted to attend the course fully, if selected.

Place:

Date:

Name & Signature of the
sponsoring Authority

(Seal of the Institution)